



FEB 02 2006 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Todd Edgar
Serial No. : 10/044,178
Filed : January 10, 2002
Title : SEMICONDUCTOR DEVICE INCORPORATING AN ELECTRICAL CONTACT TO AN INTERNAL CONDUCTIVE LAYER AND METHOD FOR MAKING THE SAME
Docket : MIO 0011 N2/40509.186
Examiner : Thao Le
Art Unit : 2814

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 30, 2006..	
John D. Reed	Reg. No. 46,506

RESPONSE UNDER 37 CFR 1.116

This response is being filed in response to an Office Action mailed January 12, 2006, having a reply due date of April 12, 2006. Reconsideration is respectfully requested in light of the remarks that begin on page 2 of this paper.